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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/699,311	10/30/2003	James C. Fye	H0005246 (002.2154)	3928
89955 HONEYWELL	7590 05/11/201 /IFL	EXAMINER		
Patent Services 101 Columbia F		SMITH, CHENEA		
P.O.Box 2245	Coau	ART UNIT	PAPER NUMBER	
Morristown, NJ	07962-2245	2421		
			NOTIFICATION DATE	DELIVERY MODE
			05/11/2010	ELECTRONIC

## Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

patentservices-us@honeywell.com DL-ACS-SM-IP@Honeywell.com docketing@ifllaw.com

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/699,311	FYE, JAMES C.		
Examiner	Art Unit		
CHENEA P. SMITH	2421		

	CHENEA P. SMITH	2421	
The MAILING DATE of this communication appear	ars on the cover sheet with the c	correspondence add	ress
THE REPLY FILED <u>20 April 2010</u> FAILS TO PLACE THIS APPL	LICATION IN CONDITION FOR AL	LOWANCE.	
1.  The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following rapplication in condition for allowance; (2) a Notice of Appe for Continued Examination (RCE) in compliance with 37 C periods:	eplies: (1) an amendment, affidavit al (with appeal fee) in compliance	t, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expiresmonths from the mailing	date of the final rejection.		
b) The period for reply expires on: (1) the mailing date of this Adno event, however, will the statutory period for reply expire la	ter than SIX MONTHS from the mailing	g date of the final rejection	n.
Examiner Note: If box 1 is checked, check either box (a) or (b MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f		FIRST REPLY WAS FI	LED WITHIN TWO
Extensions of time may be obtained under 37 CFR 1.136(a). The date of have been filed is the date for purposes of determining the period of extra under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the slate forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	on which the petition under 37 CFR 1.1 ension and the corresponding amount on the nortened statutory period for reply origi	of the fee. The appropria nally set in the final Offic	ate extension fee e action; or (2) as
2. The Notice of Appeal was filed on A brief in compl	iance with 37 CFR 41.37 must be f	iled within two months	s of the date of
filing the Notice of Appeal (37 CFR 41.37(a)), or any exten Notice of Appeal has been filed, any reply must be filed with AMENDMENTS	sion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
3. The proposed amendment(s) filed after a final rejection, b	ut prior to the date of filing a brief,	will <u>not</u> be entered be	cause
(a) They raise new issues that would require further con	•	E below);	
(b) They raise the issue of new matter (see NOTE below	•		
<ul><li>(c) ☐ They are not deemed to place the application in bett appeal; and/or</li></ul>	er form for appeal by materially rec	ducing or simplifying ti	ne issues for
(d) ☐ They present additional claims without canceling a c	orresponding number of finally reje	ected claims	
NOTE: (See 37 CFR 1.116 and 41.33(a)).	on openating manifest or mixing reju	otou olaliilo.	
4. The amendments are not in compliance with 37 CFR 1.12	1. See attached Notice of Non-Cor	mpliant Amendment (I	PTOL-324).
5. Applicant's reply has overcome the following rejection(s):			,
6. Newly proposed or amended claim(s) would be allow	owable if submitted in a separate, t	imely filed amendmer	nt canceling the
non-allowable claim(s).	<u>_</u>		
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows:		l be entered and an e	xplanation of
Claim(s) allowed:			
Claim(s) objected to: Claim(s) rejected:			
Claim(s) rejected: Claim(s) withdrawn from consideration:			
AFFIDAVIT OR OTHER EVIDENCE			
<ol> <li>The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).</li> </ol>			
9. The affidavit or other evidence filed after the date of filing a entered because the affidavit or other evidence failed to over showing a good and sufficient reasons why it is necessary	vercome <u>all</u> rejections under appea	ıl and/or appellant fail:	s to provide a
10. ☐ The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER			
<ol> <li>The request for reconsideration has been considered but Please see notes below.</li> </ol>	does NOT place the application in	condition for allowan	ce because:
12. Note the attached Information Disclosure Statement(s).	PTO/SB/08) Paper No(s)		
13.			
/John W. Miller/			
Supervisory Patent Examiner, Art Unit 2421			

- 8. In response to Applicant's arguments on page 3, lines 1-11 that "However, Applicant respectfully submits that Reynolds" fails to describe the subject matter ascribed to it by the Examiner. For example, Reynolds fails to describe "a plurality of video decoders coupled to the plurality of video channels, each video decoder coupled to a different one of the plurality of video channels." To the contrary, Reynolds" expressly describes that a multiple video feed 206 is provided to a tuner 208, which selects a single channel. The decoder 208 extracts the different signals from the video channel (Para. 0041). As such, it is clear that in Reynolds" each decoder 220/224/228 is coupled to the same video channel. Therefore, each video decoder (220, 224, 228) is not coupled to a different one of the plurality of video channels incoming from the multi-channel feed 206. A component signal from a channel cannot be reasonable construed to be a channel in and of itself. Because Reynolds" fails to describe that each video decoder is coupled to a different one of the plurality of video channels, Reynolds" fails to cure the conceded deficiencies in Machida. Therefore, amended independent claim 1 is allowable over the combination of Machida and Reynolds" for at least this reason", the Examiner respectfully disagrees. As defined by IEEE, a channel is: (1) (A) (electric communication) A single path for transmitting electric signals, usually in distinction from other parallel paths. Note: The word "path" is to be interpreted in a broad sense to include separation by frequency division or time division. The term "channel" may signify either a one-way path, providing transmission in one direction only, or a two-way path, providing transmission in two directions. Therefore, the Examiner believes that Reynolds does reasonably disclose "a plurality of video decoders coupled to the plurality of video channels, each video decoder coupled to a different one of the plurality of video channels", as claimed.
- 9. In response to Applicant's arguments on page 5, line 21 page 6, line 3 that "Applicant respectfully traverses the rejection. To render a claim obvious, the cited references must disclose each and every element of the rejected claim (see MPEP § 2143). Applicant respectfully submits that the combination of Machida in view of Reynolds, Reitmeier, Miyazaki and Mizutome fails to describe at least inputting decoded frames into a video processing pipeline via a non-blocking switch.

  In her rejection, the Examiner concedes that Machida fails to describe inputting the first and second decoded frames into a first and second video processing pipeline via a non-blocking switch (OA page 13). In fact, Machida teaches away from the use of non-blocking switch (See, paragraph 0033 (image selection means 101 selects and outputs a prescribed number of images among the input images having high priority orders)). The necessity of passing along images in order of priority is antithetical to a non-blocking switch because each image cannot be processed/displayed concurrently without hindrance", the Examiner respectfully disagrees. Machida's system discloses that a priority order is given to the input images by the screen control means, and that the number of images to be output is determined by the screen control means 106. This number, may reasonably be all of the images input. As shown by Machida in Fig. 3, all of the output images are processed by an image processing means, without regard to order or dedication to a particular image processing means. Therefore, Machida reasonably teaches a non-blocking switch, i.e., image selection means 101.